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| APPLICATION NO.      | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO.                   | CONFIRMATION NO. |
|----------------------|-------------|----------------------|---------------------------------------|------------------|
| 09/652,842           | 08/31/2000  | Vishnu K. Agarwal    | 501082.06                             | 4065             |
| 7590 02/09/2005      |             |                      | EXAMINER                              |                  |
| PAUL F. RUSYN        |             |                      | KESHAVAN, BELUR V                     |                  |
| DORSEY & WHITNEY LLP |             |                      | · · · · · · · · · · · · · · · · · · · |                  |
| 1420 FIFTH AVENUE    |             |                      | ART UNIT                              | PAPER NUMBER     |
| SUITE 3400           |             |                      | 2823                                  |                  |
| SEATTLE, W.          | A 98101     |                      |                                       |                  |

Please find below and/or attached an Office communication concerning this application or proceeding.



## UNITED STATES DEPARTMENT OF COMMERCE

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APPLICATION NO. FILING DATE FIRST NAMED INVENTOR / ATTORNEY DOCKET NO. PATENT IN REEXAMINATION

EXAMINER

**ART UNIT** 

PAPER

02042005

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**Commissioner for Patents** 

The references in the supplemental IDS of 08/07/2002 have been considered.. The findings are that none of the prior art references teaches or renders obvious a method for forming a capacitor limitations found in claims 22-24 and 77-81.

The pertinent PTO-1449 is enclosed duly initialed and signed.

MATTHEW SMITH SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800

BN 02/04/05 PTO-90C (Rev.04-03)